

CYPR-CD00179
Serial No. 09/972,319

REMARKS

Claims 1-21 are presented for consideration in the present application, which is now believed to be in condition for examination. Early notice to that effect is earnestly solicited.

Respectfully submitted,

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AMENDMENTS WITH CHANGES SHOWN:

IN THE SPECIFICATION

From page 10, line 22, through page 11, line 6:

The peripheral registers 230 may be, in general, those components which constitute parts of a microcontroller. For example, these registers may comprise programmable [systems on a chip (PSoCs (both] analog and digital [)] on-chip systems, input/output pins, and various fixed function blocks.

Thus, embodiments of the present invention provide for an efficient mechanism for feeding instructions to the microprocessor 210, single stepping instructions, programming the programmable analog and digital [PSoCs] on-chip systems, routing the programmable digital and analog [PSoCs] on-chip systems, routing input and output signals to I/O pins, and executing programs without requiring flash memory 260.